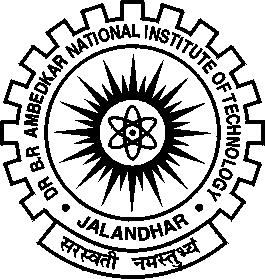
**DR B R Ambedkar National Instituteof Technology**

**(Institute of National Importance)**

**Department of Electronics & Communication Engineering**

**G T Road Bye pass, Jalandhar -144011, Punjab (India)**

**Ref No.:NITJ/ECE/ Date:**

**Advertisement No.**

Walk in interview for the following position in the Department of Electronics and Communication Engineering under the project SMDP-C2SD in VLSI Design and related Software sanctioned by Ministry of Communication and Information Technology, Government of India.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Sr No** | **Post** | **Qualification** | **Initial Duration** | **Consolidated Salary/Month** |
| **1** | **Lab Engineer** on **Temporary basis**  **(01 Post)** | 1st class M.Tech or equivalent in VLSI / Microelectronics/ ECE/ Related areas with hand on experience on VLSI CAD tools and knowledge of operating system windows/Linux and server. | 06 months\* | Rs. 34000/- |

\*This post is purely on temporary basis for the period of project and is initially for a period of 06 months. The extension for subsequent period will be subject to the satisfactory performance of the candidate on yearly basis.

**Tentative Date/Time and Venue for walk in interview: 22/07/2016** at **10:00am** in **Institute Committee Room**,**Main Building.**

Candidates are requested to send an advance soft copy of CV through mail to Chief- Investigator and Co-Chief Investigator.

Dr. R K Sarin, Dr. Ashish Raman,

Professor Assistant Professor

Chief -Investigator / SMDP-C2SD, Co-Chief Investigator / SMDP-C2SD,

ECE Department, ECE Department,

National Institute of Technology Jalandhar, National Institute of Technology Jalandhar,

-144011 -144011

Mail id: [sarinrk@nitj.ac.in](mailto:sarinrk@nitj.ac.in) Mail id: ramana@nitj.ac.in

**No TA/DA shall be given for the interview.**  The selected candidates will be required to join immediately.

**Information to the candidates**

Duties and responsibilities for Lab engineer

1) Handling theory as well as laboratory courses on VLSI systems for UG and PG students

2) Evaluation of the students and awarding grades on the theory and laboratory subjects handled

3) Guiding the UG/PG students on their project work in VLSI design & testing

4) Undergoing training on the advanced VLSI design topics under the Learning Enhancement programme (LEP) to be conducted at the VLSI man power resource centers.

5) Preparation and submission of project reports to the project funding and monitoring agencies

6) Developing the skills on advanced VLSI CAD tools and guide the students on chip design under SMDP-C2SD.

7) Any other duties that may be assigned by the project coordinator and co-coordinator.

8) Installing and Maintaining CAD tools in Windows and Linux Environment.

9) Designing the proposed prototype of the project.